



# SRI VENKATESWARA COLLEGE OF ENGINEERING

(AUTONOMOUS)

Karakambadi Road, Tirupati-517507

R 20

EXAMINATION BRANCH

## M.Tech III Semester (R20) Supplementary Examinations April-2026 TIME TABLE

Timings: 10.00 AM TO 01.00 PM

Date/Day	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
13-04-2026	Bi-CMOS Technology and Applications EC20DPE301	Mobile Applications and Services CS20DPE301
	Optimization Techniques and Applications in VLSI Design EC20DPE302	High Performance Computing CS20DPE302
	System on Chip Architecture EC20DPE303	Optimization Techniques CS20DPE303
16-04-2026	Business Analytics EC20DOE304	Quantum Computing CS20DOE304
	Composite Materials EC20DOE305	Industrial IOT CS20DOE305
	Industrial Safety EC20DOE306	Cyber Security CS20DOE306

Note: Any discrepancy in this time table may be brought to the notice of the under signed immediately.

*Y. A. Sivaprasad*  
Controller of Examinations  
04-04-2026

*Sri Jay*  
Principal  
6/4/26

